

Fig. 3

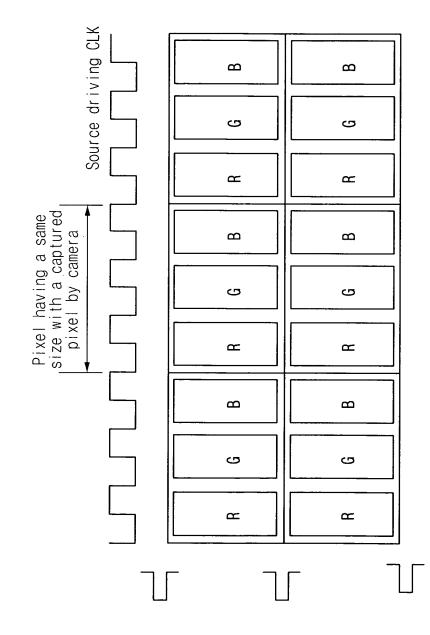


Fig. 4

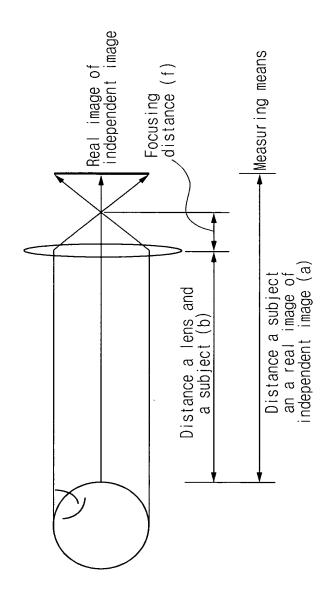
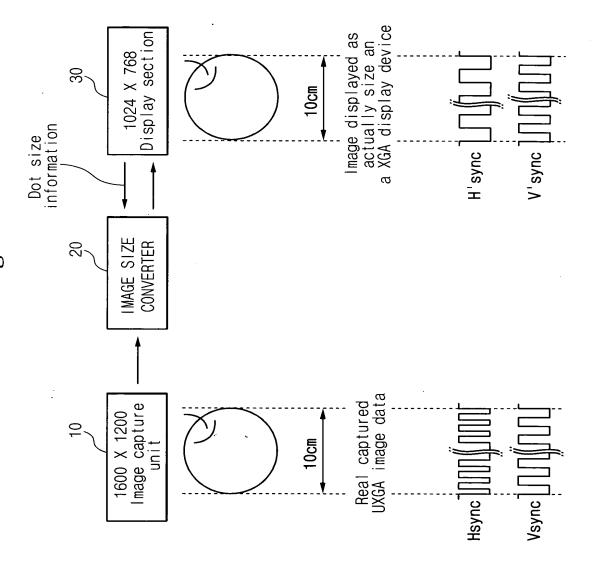


Fig. 5





Source(Data) driver D3 D2 300 LOAD Sig Hstart (0:N) (N:0) Gate CIK Vstart 301 R (0:N) lmage size converter (N:0) 9 B (0:N) 200 Hsync Vsync CCD photo-graphing unit 120

Hig. 6